**BERKELEY LAB** 

# The Promise of Superconducting Quantum Information Processing

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quantumsystemsaccelerator.org



Advanced Quantum Testbed Computational Research Division (CRD) AQT at Berkeley Lab

aqt.lbl.gov



qnl.berkeley.edu



**CEC/ICMC 21** Plenary July 19, 2021 IEEE CSC & ESAS SUPERCONDUCTIVITY NEWS FORUM (global edition), January 2022. Plenary presentation M10r-PL given at CEC-ICMC 2021, July 19-23, 2021, Virtual. Quantum: Thought Experiment to Technical Revolution



### **The Quantum World Around Us**

A MRI Scan Relies on Quantum Mechanics!

Water molecules have two hydrogens which have nuclear spin (up or down)



### QUANTUM SYSTEMS CAN EXIST IN MANY DIFFERENT CONFIGURATIONS, EVEN IF WE CAN'T OBSERVE ALL OF THEM!

### **The Power of Entanglement**

- Let's build a computer one spin (quantum bit) at a time !
- Unlike MRI which measures <u>average</u> properties of a group of spins, we need to address each spin individually

- Measurement reveals state to be
- If we don't observe, state is (a · + b · ) and described by 2 numbers {a,b}
- Adjacent bit is (c · + d · ) and described by 2 numbers {c,d}
- Couple these two bits and consider product: (a · + b · ) X (c · + d · )

cannot describe

If a = 0, lose **ac** · **††** If d = 0, lose **bd** ·

2<sup>N</sup> >> 2N: NEED MORE NUMBERS THAN PARTICLES IN THE UNIVERSE TO DESCRIBE ~ 300 ENTANGLED QUBITS

Entangled

State

### The Quantum Information Paradigm Shift



(Z370: www.pcworld.com)

• Start with a good switch...



- Advanced materials science, electromagnetism, and thermodynamics at the nm scale
- Different functional units (processor, memory,..)
- Advanced packaging/controls
- Well matched algorithms

### **QUANTUM IS FUNDAMENTALLY DIFFERENT AT ALL LEVELS!**

## Potential Quantum Advantage

- Pattern detection / Fourier analysis
- Efficiently searching a large database
- Finding energy (cost) minima
- Matrix math
  - Systems of equations
  - Machine learning
  - Diagonalization

### **Challenges:**

- *i.* Decoherence limits complexity (need >100 gates with 99.9 fidelity)
- *ii.* Error correction / resource scaling (data input/output, error correction, ...)

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	Machine learning and artificial intelligence, such as neural networks	?	IBM	Telstra
	<ul> <li>Search</li> <li>Bidding strategies for advertisements</li> </ul>		Alibaba	Baidu
	<ul> <li>Cybersecurity</li> <li>Online and product marketing</li> </ul>		Google	Samsung
	Software verification and validation		Microsoft	
Industrial	<ul> <li>Logistics: scheduling, planning, product distribution, routing</li> <li>Automotive: traffic simulation, e-charging station and parking</li> </ul>	Y	Airbus	BMW
goods	search, autonomous driving • Semiconductors: manufacturing, such as chip layout		NASA	Volkswagen
	optimization • Aerospace: B&D and manufacturing such as fault-analysis		Northrop Grumman	Lockheed Martin
	stronger polymers for airplanes		Daimler	Honeywell
	cell research, more-efficient materials for solar cells, and		Baytheon	Bosch
	property engineering uses such as OLEDS		Raytheon	DUSCII
Chemistry	Catalyst and enzyme design, such as nitrogenase     Pharmaceuticals R&D such as faster drug discovery	$\rangle$	BASF	JSR
and Pharma	Bioinformatics, such as genomics		Biogen	DuPont
	Patient diagnostics for health care, such as improved diagnostic capability for MRI		Chemical	Amgen
	Trading strategies	ς	I.P. Morgan	Barclays
<b>Finance</b>	Portfolio optimization     Accet priving	ſ	Commonwealth	Goldman
	Risk analysis		Bank	Sachs
	Fraud detection     Market simulation			
Energy	Network design     Energy distribution	2	Dubai Electricity &	BP
	Oil well optimization		Water Authority	

### The New York Times

## Why Google's Quantum Supremacy Milestone Matters

The company says its quantum computer can complete a calculation much faster than a supercomputer. What does that mean?

#### By Scott Aaronson

Dr. Aaronson is the founding director of the Quantum Information Center at the University of Texas at Austin.

Oct. 30, 2019







Google researchers in Santa Barbara, California, say their advance may lead to near-term applications of quantum computers. ISTOCK.COM/JHVEPHOTO

# IBM casts doubt on Google's claims of quantum supremacy

By Adrian Cho Oct. 23, 2019 , 5:40 AM



Google A.I. Quantum's Sycamore processor. Erik Lucero/Google

### **From Art to Architecture**



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# SUPERCONDUCTING QUANTUM PROCESSORS

### **Accessing the Quantum World**



- Combination of R, L, C (linear or nonlinear)
- Excite with voltages / currents (AC or DC)
- Classically, these quantitates can take on any continuous values

### QUANTUM MECHANICS SAYS THESE QUANTITIES CAN BE DISCRETE!



## **Starting From a Nonlinear Oscillator**



- Classical harmonic oscillator (parabolic potential) : all energies (currents) are allowed
- Quantum harmonic oscillator: only certain energies (currents) are allowed
- Tunnel junction (cosine potential) →
   Nonlinear, isolate 0, 1



Al/AlOx/Al Josephson tunnel junctions

## **Qubit Readout with Microwave Reflectometry**



## **Multiqubit Chip Quantum Coherence**



- Bit flip ~ 10 ns, Entangling gate ~ 100-500 ns
- Improve materials
- Use noise resilient circuits (symmetry/topology)
- Active noise mitigation



# **BETTER QUBITS**



## **Controlling Surfaces and Interfaces**

## Localization and reduction of superconducting quantum coherent circuit losses

M. Virginia P. Altoé<sup>1\*</sup>, Archan Banerjee<sup>2,3\*</sup>, Cassidy Berk<sup>1\*</sup>, Ahmed Hajr<sup>3,4,5\*</sup>, Adam Schwartzberg<sup>1</sup>, Chengyu Song<sup>1</sup>, Mohammed Al Ghadeer<sup>6</sup>, Shaul Aloni<sup>1</sup>, Michael J. Elowson<sup>1</sup>, John Mark Kreikebaum<sup>2,3</sup>, Ed K. Wong<sup>1</sup>, Sinead Griffin<sup>1</sup>, Saleem Rao<sup>6</sup>, Alexander Weber-Bargioni<sup>1</sup>, Andrew M. Minor<sup>1,7</sup>, David I. Santiago<sup>3,4</sup>, Stefano Cabrini<sup>1</sup>, Irfan Siddiqi<sup>2,3,4</sup>, and D. Frank Ogletree<sup>1†</sup>





### Next Steps: Decoherence due to Non-Equilibrium Phenomena

- Quasiparticles & Phonons (localize effects of pair breaking)
- Fluctuations of Coherence (correlate with JJ non-uniformity & spectral diffusion of glassy films)
- Strain Induced Magnetism & Loss/Noise (verify presence & mitigate)

## Noise Tailoring using Randomized Compiling



 $\begin{array}{c} |0\rangle - \underline{X} - [\underline{Y}] - [\underline{Y}] - \underline{T} - [\underline{X}] + [\underline{I}] - \underline{H} - \underline{A} \\ |0\rangle - \underline{H} - [\underline{Z}] - [\underline{I}] - \underline{Z} - [\underline{X}] - [\underline{X}] - \underline{A} \\ |0\rangle - \underline{Z} - [\underline{Z}] - [\underline{Z}] - [\underline{I}] - [\underline{I}] - [\underline{I}] - \underline{A} \\ |0\rangle - \underline{X} - [\underline{X}] - [\underline{X}] - [\underline{X}] - [\underline{Z}] - \underline{A} \\ |0\rangle - \underline{X} - [\underline{X}] - [\underline{X}] - [\underline{X}] - \underline{A} \\ |0\rangle - \underline{Y} - [\underline{Z}] - [\underline{Z}] - \underline{H} - [\underline{I}] - [\underline{I}] - \underline{A} \end{array}$ 



### **Noise Protected Qubit Architectures**



# CRYOPACKAGING

10 GHz slotline mode

**Bondpad Width** 

## **Quantum Chatter**

**Spurious mode** 

Identification

Crosstalk

#5

#6

#7

#8

100x100 um<sup>2</sup>

bondpad

2 ×10<sup>3</sup>

0 dB

Power crosstalk

-80 dB

100 μm

500

0

-500

×10<sup>3</sup>

#4

#3

#2

#1



## Cryopackaging

IEEE CSC & ESAS SUPERCONDUCTIVITY NEWS FORUM (global edition), January 2022. Plenary presentation M10r-PL given at CEC-ICMC 2021, July 19-23, 2021, Virtual.



12-line version for Trailblazer chip

GEN2 PACKAGING WAS DESIGNED TO ACCOMMODATE UP TO 20 X 20 mm<sup>2</sup> CHIPS WITH UP TO 40 RF LINES AND STRIPLINE PRINTED CIRCUIT BOARDS TO MININIMIZE CROSS-TALK



### SIMULATIONS

- Input port matching < -25dB over 2-8 GHz band (-30dB at 5 GHz)
- Next-neighbor cross-talk levels is below -55dB over 2-8 GHz band for the closest pair of lines on the Trailblazer chip and below -72dB for other pairs of lines





## Experimental Stage

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### UCB-TWP in E8 16x t supe read-

UCB-designed TWPAs (@10 mK) in E&M shielding

16x total – one per superconducting read-out line

#### "CONTROL VOLUME"

Bracketry & breadboard plates for supporting and thermalizing microwave components (filters, isolators, circulators, directional couplers, etc.)

### "SAMPLE VOLUME"

Separated into four identical segments

Each segment has access to 40 RF lines, including 4 superconducting lines

Breadboard-based design allows extreme flexibility in building various microwave control and readout solutions

## **Tyranny of Wires!**

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Need to reduce wire count !

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- Need to reduce
   wire complexity
  - Quantum data transmission & conversion
    - optical
    - acoustic
    - classical analog
    - classical digital
    - Cryogenic data processing ?

# **EXECUTING ALGORITHMS WITH NOISY HARDWARE**

## **QITE Algorithm**

Principle of Quantum Imaginary Time Evolution

Motta et al., *Nature Physics* 16, 205-210 (2020) S. Sun et al., *PRX QUANTUM* 2, 010317 (2021) K. Yeter-Aydeniz et al., *npj Quantum Information* **6**, 63 (2020)

Time evolution under a Hamiltonian: 
$$|\Psi(t)\rangle = \sum_{m} c_m e^{-iE_m t/\hbar} |\Phi_m|$$
  
Imaginary time:  $t \rightarrow i\beta$ 

$$|\Psi(\beta)\rangle = \sum_{m} c_{m} e^{-E_{m}\beta/\hbar} |\Phi_{m}\rangle \underset{\beta \to \infty}{\sim} c_{0} e^{-E_{0}\beta/\hbar} |\Phi_{0}\rangle \text{ Ground state}$$
  
Non unitary evolution

- Implement a non-unitary evolution?
  - Trotter-Suzuki in the imaginary time step
  - Unitarize at each step
  - The Generator A<sub>1</sub> is calculated using a linear system of equations
    - No ansatz
    - No classically hard optimization
    - Measurements needed depend on the Hamiltonian
    - Easy extension to thermal states

- Not a fixed depth algorithm

$$\left|\Psi_{n+1}\right\rangle = \frac{e^{-\Delta\tau\hat{H}/\hbar}\left|\Psi_{n}\right\rangle}{\left|\left|e^{-\Delta\tau\hat{H}/\hbar}\left|\Psi_{n}\right\rangle\right.\right|\right|} = e^{-i\Delta\tau\hat{A}_{n}/\hbar}\left|\Psi_{n}\right\rangle$$



 $V_{!} = \operatorname{Exp}\left(i' \quad x_{\#}P\right)$ 

LBNL - QSEARCH

# CNOT gates

N	QISKIT	QFAST	QSEARCH
2	3	3	3
3	30-35	10-12	7-12
4	160-200	70-80	30-50

step

Generators

 $\chi_{\#}$ 

0.1

-0.5

...

Ρ

XX

XZ

...



- 7 measurements (7 \* 5000 shots) at each step
- ~ 12 CZ gates at each step
- 20 steps here
  - 3 Qubit Dry run, AQT hardware

Motta *et al. Nature Physics* **16**, 205-210 (2020)







$$H = J X + i J + h Z = i J + i J + h Z = i Z$$

$$hiji + i J + h + i Z = i Z$$



### **QITE TFIM 3 Sites: Ground and 1st Excited State**



• Remaining error doesn't seem to depend on Hamiltonian

## The Team

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ASSEMBLING RESEARCHERS IN PHYSICS, CHEMISTRY, COMPUTER SCIENCE, AND ENGINEERING TO EXPLORE THE QUANTUM FRONTIER

### **Research Staf**

- Anastasia Butko
- Sinead Griffin
- Gang Huang
- Costin lancu

### **Engineering Staff**

- Virginia Altoe
- Lawrence Doolittle
- Wim Lavrijsen
- Thorsten Stezelberger

### **Postdoctoral Researchers**

- Archan Banerjee
- Cassidy Berk
- Machiel Sebastiaan Blok
- Gerwin Koolstra

- Xiaotao Liu
- Jie "Roger" Luo
- Alexis Morvan
- Kasra Nowrouzi
- Jean-Loup Ville
- Yilun Xu

### **Graduate Students**

- Larry Chen
- Trevor Chistolini
- Akel Hashim
- John Mark Kreikebaum
- Bradley Mitchell
- Marie Lu
- William Livingston





Massachusetts Institute of Technology













# **Advanced Quantum Testbed**

(aqt.lbl.gov)



**Superconducting Quantum Processors at the Entanglement Frontier** \*\*\*\* Commercial Team: LBNL, UC Berkeley, Bleximo, LBNL/ATAP **3D Integrated Quantum** 1 mW Dilution fridge Cold stage Processor Units (QPU) MIT-LL Control Control E ...... 111 111 1 FFFFMM XY-Drive BUS SQUID **ble**ximo Bus Mode Pump BUS SQ Qubits

## **Quantum Systems Accelerator**

Atomic

### (quantumsystemsaccelerator.org)



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Catalyzing national leadership in quantum information science to co-design the algorithms, quantum devices, and engineering solutions needed to deliver certified quantum advantage in Department of Energy scientific applications.

Trapped **Tweezers** lons Sandia MIT LINCOLN LABORATORY National rrrr Laboratories **BERKELEY LA** Berkeley Caltech HARVARD UNIVERSITY **G7** Duke Superconducting Rydberg Circuits Simulators PRATT SCHOOL OF University of Colorado Engineering Boulder UNIVERSITÉ DE SHERBROOKE MARYLAND ន USC The University of Texas at Austin



## **Future Directions**

### **Critical Enablers**

- Resource efficient error detection/correction
- Robust fabrication of high coherence devices (metals, insulators, 3D integration)
- Circuit elements beyond JJs (super-inductances, phase slip junctions, ...)
- Suppression of nonequilibrium excitations (quasiparticles, phonons, ...)
- Quantum signal processing hardware (multiplex, convert, interconnect...)
- Flexible cryogenic systems (EMI shielding, modularity, electronics, wiring,...)

### **Open Questions**

- What is the optimal balance between noise-protection vs. materials perfection?
- Is it possible to achieve quantum advantage with noisy hardware? What algorithms? benchmarks?
- What problems are best suited for digital computation vs. analog emulation?
- Can we efficiently simulate dynamics problems? What are the useful ones?